FIG. 1

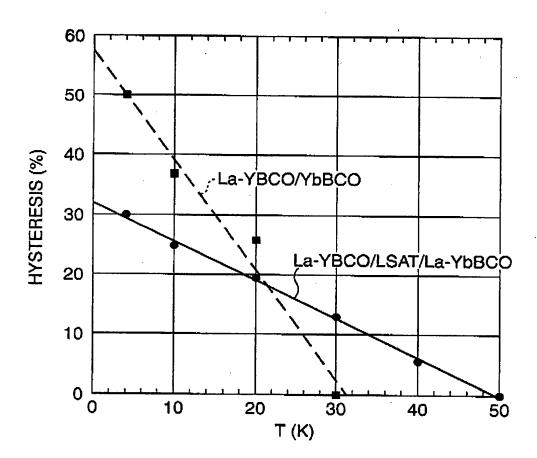
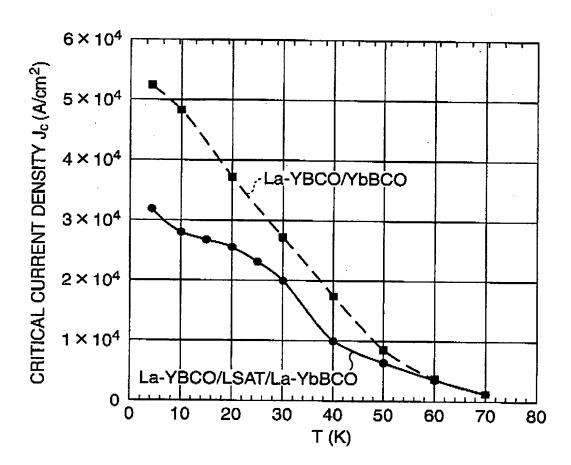


FIG. 2



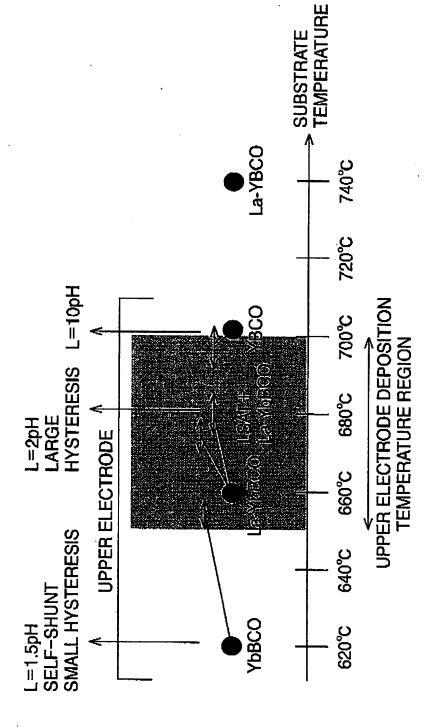
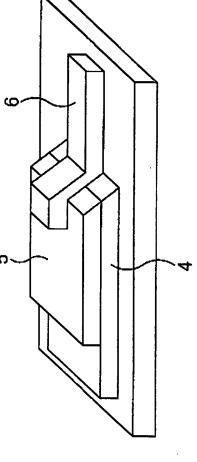


FIG. 3

<JUNCTION FOR SFQ CIRCUIT>





<JUNCTION FOR LATCH DRIVER CIRCUIT>

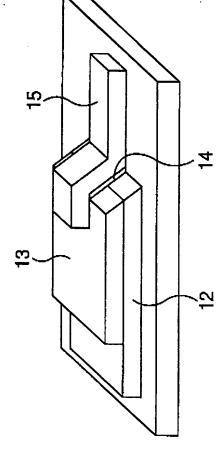


FIG. 4

JUNCTION FOR SFQ CIRCUIT>

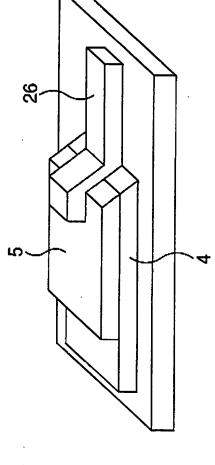


FIG. 5A

<JUNCTION FOR LATCH DRIVER CIRCUIT>

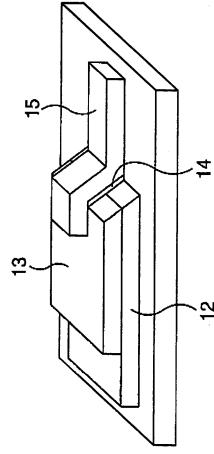
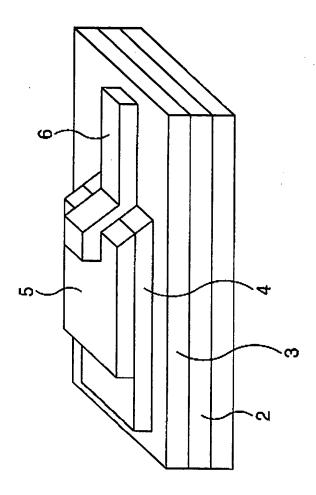


FIG. 5B

Title: SUPERCONDUCTING CIRCUIT By: Tsunehiro HATO, et al. Docket No.: 040156

FIG. 6

<JUNCTION FOR SFQ CIRCUIT>



Title: SUPERCONDUCTING CIRCUIT By: Tsunehiro HATO, et al. Docket No.: 040156

FIG. 7

<JUNCTION FOR SFQ CIRCUIT>

